

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	microprocessor same secur\$3 same environment and JTAG	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 15:23
L2	137	microprocessor same secur\$3 and JTAG	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 15:28
L3	11	L2 and @ad<"20000201"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 15:29
L4	0	L3 and SOC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 15:27
L5	4	microprocessor same secur\$3 same SOC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 15:28
L6	0	microprocessor same secur\$3 and system adj on adj chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 15:29
L7	0	microprocessor same secur\$3 and system adj on near3 chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 15:29
L8	313	microprocessor same secur\$3 and system near3 chip	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 15:29
L9	43	L8 and JTAG	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 15:29
L10	5	L9 and @ad<"20000201"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:48

L11	2	"5892900".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 16:48
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Day : Thursday
Date: 10/20/2005


PALM INTRANET

Time: 18:21:10

Inventor Name Search Result

Your Search was:

Last Name = NORTH

First Name = GREGORY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09495813	Not Issued	41	02/01/2000	Circuits, system and methods for information privatization in personal electronic appliances	NORTH, GREGORY ALLEN
09591659	6912557	150	06/09/2000	MATH COPROCESSOR	NORTH, GREGORY ALLEN
09608055	6754784	150	06/30/2000	METHODS AND CIRCUITS FOR SECURING ENCACHED INFORMATION	NORTH, GREGORY ALLEN
09608072	Not Issued	161	06/30/2000	Methods for synthesizing translation tables and systems using the same	NORTH, GREGORY ALLEN
09609023	Not Issued	41	06/30/2000	Hand-held audio decoder	NORTH, GREGORY ALLEN
09609597	Not Issued	161	06/30/2000	Methods and circuits for selectively operating a system in a secure environment	NORTH, GREGORY ALLEN
09821897	Not Issued	161	03/30/2001	System on a chip with multiple power planes and associate power management methods	NORTH, GREGORY ALLEN
09822645	6622208	150	03/30/2001	SYSTEM AND METHODS USING A SYSTEM-ON-A-CHIP WITH SOFT CACHE	NORTH, GREGORY ALLEN
10920879	Not Issued	41	08/18/2004	Math coprocessor	NORTH, GREGORY ALLEN
10922219	Not Issued	41	08/19/2004	Math coprocessor	NORTH, GREGORY ALLEN
08797232	6055619	150	02/07/1997	CIRCUITS, SYSTEM, AND METHODS FOR PROCESSING MULTIPLE DATA STREAMS	NORTH, GREGORY ALLEN
09841505	Not Issued	95	04/24/2001	CMOS TAPERED GATE AND SYNTHESIS METHOD	NORTHROP, GREGORY A.
10842589	Not	30	05/10/2004	Method for tuning a digital design	NORTHROP,

	Issued			for synthesized random logic circuit macros in a continuous design space with optional insertion of multiple threshold voltage devices	GREGORY A.
10917193	Not Issued	30	08/12/2004	Physical design system and method	NORTHROP, GREGORY A.
11055905	Not Issued	30	02/11/2005	Schematic driven placement method and program product for custom VLSI circuit design	NORTHROP, GREGORY A.
06928496	Not Issued	161	11/07/1986	SURFACE INTERFEROMETER	NORTHROP, GREGORY A.
10687475	Not Issued	30	10/16/2003	Method of extracting properties of back end of line (BEOL) chip architecture	NORTHROP, GREGORY ALLEN

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	Search
	<input type="text" value="north"/>	<input type="text" value="gregory"/>	

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